

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L5	51	instruction with breakpoint near2 bit	US-PGPUB; USPAT	OR	ON	2008/08/04 17:40
L7	42	(append\$3 attach\$4 add \$3) with (breakpoint break-point "break point") with (bit flag indicat\$3) and (g06f\$.ipc. debug\$4)	US-PGPUB; USPAT	OR	ON	2008/08/04 17:40
S2	699	712/227.ccls.	US-PGPUB; USPAT	OR	ON	2007/06/13 10:26
S3	971	717/124.ccls.	US-PGPUB; USPAT	OR	ON	2007/06/13 10:25
S4	667	717/127.ccls.	US-PGPUB; USPAT	OR	ON	2007/09/13 10:27
S5	292	717/129.ccls.	US-PGPUB; USPAT	OR	ON	2007/06/13 10:25
S6	48	instruction with breakpoint near2 bit	US-PGPUB; USPAT	OR	ON	2007/06/13 10:32
S7	565	debug\$4 near3 control near2 register	US-PGPUB; USPAT	OR	ON	2007/06/14 12:22
S8	9	instruction with breakpoint near2 field	US-PGPUB; USPAT	OR	ON	2007/06/13 12:22
S9	111	instruction with debug\$4 near2 (bit field)	US-PGPUB; USPAT	OR	ON	2007/06/13 10:34
S10	45	instruction near5 debug\$4 near2 (bit field)	US-PGPUB; USPAT	OR	ON	2007/06/13 10:34
S11	64	instruction with break near2 (bit field)	US-PGPUB; USPAT	OR	ON	2007/06/13 12:25
S12	3	instruction with (break-point "break point") near2 (bit field)	US-PGPUB; USPAT	OR	ON	2007/06/13 12:26
S13	22	instruction with (break-point breakpoint "break point") near2 (bit field)	USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/13 12:39
S14	9	("5165027" "5404473" "5442757" "5479616" "5479652" "5481685" "5491793" "5530804" "5537559").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/06/13 12:37
S15	32	instruction with (break-point breakpoint "break point") near2 flag	US-PGPUB; USPAT	OR	ON	2007/06/13 12:42

S16	10	instruction with (break-point breakpoint "break point") near2 mark\$3	US-PGPUB; USPAT	OR	ON	2007/06/13 12:42
S17	11	("4635193" "4747045" "5249278" "5321828" "5371894" "5479652" "5533192" "5537550" "5572667" "5691920" "5704034").PN	US-PGPUB; USPAT; USOCR	OR	ON	2007/06/13 12:44
S18	2	debug\$4 near3 control near2 register same (enabl \$3 disabl\$3) same step same run	US-PGPUB; USPAT	OR	ON	2007/06/14 12:23
S19	2	(append\$3 attach\$3) with debug\$4 with enable with (bit field flag)	US-PGPUB; USPAT	OR	ON	2007/06/14 12:41
S20	29	debug\$4 same (((enabl\$3 disabl\$3) and step and (run\$4 continu\$5)) with (bit field flag))	US-PGPUB; USPAT	OR	ON	2007/06/14 12:45
S21	15	debug\$4 same (((enabl\$3 disabl\$3) and (single near2 step\$4) and (run\$4 continu\$5)) with (bit field flag))	US-PGPUB; USPAT	OR	ON	2007/06/14 12:54
S22	81	"debug status register"	US-PGPUB; USPAT	OR	ON	2007/06/14 12:55
S23	56	"debug status register" same step\$4	US-PGPUB; USPAT	OR	ON	2007/06/14 12:55
S24	19	"debug status register" same step\$4 same (enabl \$3 disabl\$3)	US-PGPUB; USPAT	OR	ON	2007/06/14 12:55
S25	26	(single adj step\$4 near5 (bit flag field)) same (((enabl\$3 disabl\$3) near5 (bit flag field)) same (control near2 register)	US-PGPUB; USPAT	OR	ON	2007/06/15 12:07
S26	1	"6016555".pn.	US-PGPUB; USPAT	OR	ON	2007/06/15 12:07
S27	9	("4429368" "4701921" "4860290" "5084814" "5535331" "5564028" "5724563" "5778208" "5778230").PN	US-PGPUB; USPAT; USOCR	OR	ON	2007/06/15 12:32
S28	5	stop near3 (bit field flag) same debug\$4 with (status control) with register	US-PGPUB; USPAT; USOCR	OR	ON	2007/06/15 12:33
S29	298	714/34.ccls	US-PGPUB; USPAT	OR	ON	2007/06/15 14:55

S30	690	717/127.ccls.	US-PGPUB; USPAT	OR	ON	2007/09/12 13:59
S31	299	717/129.ccls.	US-PGPUB; USPAT	OR	ON	2007/09/12 13:59
S32	48	instruction with breakpoint near2 bit	US-PGPUB; USPAT	OR	ON	2007/09/12 13:59
S33	9	instruction with breakpoint near2 field	US-PGPUB; USPAT	OR	ON	2007/09/12 13:59
S34	586	debug\$4 near3 control near2 register	US-PGPUB; USPAT	OR	ON	2007/09/12 13:59
S35	48	instruction near5 debug\$4 near2 (bit field)	US-PGPUB; USPAT	OR	ON	2007/09/12 13:59
S36	116	instruction with debug\$4 near2 (bit field)	US-PGPUB; USPAT	OR	ON	2007/09/12 13:59
S37	716	712/227.ccls.	US-PGPUB; USPAT	OR	ON	2007/09/12 13:59
S38	1025	717/124.ccls.	US-PGPUB; USPAT	OR	ON	2007/09/12 13:59
S39	10	instruction with (break- point breakpoint "break point") near2 mark\$3	US-PGPUB; USPAT	OR	ON	2007/09/12 13:59
S40	9	("5165027" "5404473" "5442757" "5479616" "5479652" "5481685" "5491793" "5530804" "5537559").FN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/09/12 13:59
S41	64	instruction with break near2 (bit field)	US-PGPUB; USPAT	OR	ON	2007/09/12 13:59
S42	32	instruction with (break- point breakpoint "break point") near2 flag	US-PGPUB; USPAT	OR	ON	2007/09/12 13:59
S43	11	("4635193" "4747045" "5249278" "5321828" "5371894" "5479652" "5533192" "5537550" "5572667" "5691920" "5704034").FN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/09/12 13:59
S44	22	instruction with (break- point breakpoint "break point") near2 (bit field)	USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/12 13:59
S45	3	instruction with (break- point "break point") near2 (bit field)	US-PGPUB; USPAT	OR	ON	2007/09/12 13:59
S46	2	debug\$4 near3 control near2 register same (enab \$3 disabl\$3) same step same run	US-PGPUB; USPAT	OR	ON	2007/09/12 13:59

S47	2	(append\$3 attach\$3) with debug\$4 with enable with (bit field flag)	US-PGPUB; USPAT	OR	ON	2007/09/12 13:59
S48	30	debug\$4 same (((enabl\$3 disabl\$3) and step and (run\$4 continu\$5)) with (bit field flag))	US-PGPUB; USPAT	OR	ON	2007/09/12 13:59
S49	15	debug\$4 same (((enabl\$3 disabl\$3) and (single near2 step\$4) and (run\$4 continu\$5)) with (bit field flag))	US-PGPUB; USPAT	OR	ON	2007/09/12 13:59
S50	83	"debug status register"	US-PGPUB; USPAT	OR	ON	2007/09/12 13:59
S51	57	"debug status register" same step\$4	US-PGPUB; USPAT	OR	ON	2007/09/12 13:59
S52	19	"debug status register" same step\$4 same (enabl\$3 disabl\$3)	US-PGPUB; USPAT	OR	ON	2007/09/12 13:59
S53	1	"6016555".pn.	US-PGPUB; USPAT	OR	ON	2007/09/12 13:59
S54	26	(single adj step\$4 near5 (bit flag field)) same (((enabl\$3 disabl\$3) near5 (bit flag field)) same (control near2 register)	US-PGPUB; USPAT	OR	ON	2007/09/12 13:59
S55	9	("4429368" "4701921" "4860290" "5084814" "5535331" "5564028" "5724563" "5778208" "5778230").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/09/12 13:59
S56	5	stop near3 (bit field flag) same debug\$4 with (status control) with register	US-PGPUB; USPAT; USOCR	OR	ON	2007/09/12 13:59
S57	306	714/34.ccls.	US-PGPUB; USPAT	OR	ON	2007/09/12 14:10
S58	61	huisman.xa.	US-PGPUB; USPAT	OR	ON	2007/09/12 14:10
S60	758	712/227.ccls.	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S61	1172	717/124.ccls.	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S62	746	717/127.ccls.	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S63	332	717/129.ccls.	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S64	51	instruction with breakpoint near2 bit	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41

S65	616	debug\$4 near3 control near2 register	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S66	9	instruction with breakpoint near2 field	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S67	121	instruction with debug\$4 near2 (bit field)	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S68	49	instruction near5 debug\$4 near2 (bit field)	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S69	64	instruction with break near2 (bit field)	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S70	3	instruction with (break-point "break point") near2 (bit field)	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S71	22	instruction with (break-point breakpoint "break point") near2 (bit field)	USOCR; FPRS; EPC; JPC; DERWENT; IBM_TDB	OR	ON	2008/04/15 18:41
S72	9	("5165027" "5404473" "5442757" "5479616" "5479652" "5481685" "5491793" "5530804" "5537559").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 18:41
S73	33	instruction with (break-point breakpoint "break point") near2 flag	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S74	10	instruction with (break-point breakpoint "break point") near2 mark\$3	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S75	11	("4635193" "4747045" "5249278" "5321828" "5371894" "5479652" "5533192" "5537550" "5572667" "5691920" "5704034").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 18:41
S76	2	debug\$4 near3 control near2 register same (enabl\$3 disabl\$3) same step same run	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S77	2	(append\$3 attach\$3) with debug\$4 with enable with (bit field flag)	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S78	35	debug\$4 same (((enabl\$3 disabl\$3) and step and (run\$4 continu\$5)) with (bit field flag))	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S79	15	debug\$4 same (((enabl\$3 disabl\$3) and (single near2 step\$4) and (run\$4 continu\$5)) with (bit field flag))	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41

S80	83	"debug status register"	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S81	57	"debug status register" same step\$4	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S82	19	"debug status register" same step\$4 same (enabl \$3 disabl\$3)	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S83	26	(single adj step\$4 near5 (bit flag field)) same ((enabl\$3 disabl\$3) near5 (bit flag field)) same (control near2 register)	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S84	1	"6016555".pn.	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S85	9	("4429368" "4701921" "4860290" "5084814" "5535331" "5564028" "5724563" "5778208" "5778230").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 18:41
S86	6	stop near3 (bit field flag) same debug\$4 with (status control) with register	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 18:41
S87	322	714/34.ccls.	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S88	746	717/127.ccls.	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S89	332	717/129.ccls.	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S90	51	instruction with breakpoint near2 bit	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S91	9	instruction with breakpoint near2 field	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S92	616	debug\$4 near3 control near2 register	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S93	49	instruction near5 debug\$4 near2 (bit field)	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S94	121	instruction with debug\$4 near2 (bit field)	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S95	758	712/227.ccls.	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S96	1172	717/124.ccls.	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S97	10	instruction with (break- point breakpoint "break point") near2 mark\$3	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41

S98	9	("5165027" "5404473" "5442757" "5479616" "5479652" "5481685" "5491793" "5530804" "5537559").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 18:41
S99	64	instruction with break near2 (bit field)	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S100	33	instruction with (break- point breakpoint "break point") near2 flag	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S101	11	("4635193" "4747045" "5249278" "5321828" "5371894" "5479652" "5533192" "5537550" "5572667" "5691920" "5704034").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 18:41
S102	22	instruction with (break- point breakpoint "break point") near2 (bit field)	USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/15 18:41
S103	3	instruction with (break- point "break point") near2 (bit field)	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S104	2	debug\$4 near3 control near2 register same (enab \$3 disab\$3) same step same run	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S105	2	(append\$3 attach\$3) with debug\$4 with enable with (bit field flag)	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S106	35	debug\$4 same (((enab\$3 disab\$3) and step and (run\$4 continu\$5)) with (bit field flag))	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S107	15	debug\$4 same (((enab\$3 disab\$3) and (single near2 step\$4) and (run\$4 continu\$5)) with (bit field flag))	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S108	83	"debug status register"	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S109	57	"debug status register" same step\$4	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S110	19	"debug status register" same step\$4 same (enab \$3 disab\$3)	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S111	1	"6016555".pn.	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41

S112	26	(single adj step\$4 near5 (bit flag field)) same ((enabl\$3 disabl\$3) near5 (bit flag field)) same (control near2 register)	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S113	9	"4429368" "4701921" "4860290" "5084814" "5535331" "5564028" "5724563" "5778208" "5778230").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 18:41
S114	6	stop near3 (bit field flag) same debug\$4 with (status control) with register	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 18:41
S115	322	714/34.cds.	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S116	68	huisman.xa.	US-PGPUB; USPAT	OR	ON	2008/04/15 18:41
S117	527	instruction with load\$3 with register with ("instruction memory" I-cache "instruction RAM")	US-PGPUB; USPAT	OR	ON	2008/04/15 18:44
S118	3	instruction adj load\$3 near3 register near4 ("instruction memory" I-cache "instruction RAM")	US-PGPUB; USPAT	OR	ON	2008/04/15 18:44
S119	0	load\$3 near2 into near2 ("instruction memory" I-cache "instruction RAM")	US-PGPUB; USPAT	OR	ON	2008/04/15 18:49
S120	0	register with stor\$3 near2 (into in to) near2 ("instruction memory" I-cache "instruction RAM")	US-PGPUB; USPAT	OR	ON	2008/04/15 18:50
S121	46	stor\$3 near2 (into in to) near2 ("instruction memory" I-cache "instruction RAM")	US-PGPUB; USPAT	OR	ON	2008/04/15 18:50
S123	2	(breakpoint break-point "break point") with without near4 address\$3 near2 compar\$4	US-PGPUB; USPAT	OR	ON	2008/04/15 19:11
S124	166	(breakpoint break-point "break point") with address \$3 near2 compar\$4	US-PGPUB; USPAT	OR	ON	2008/04/15 19:13
S125	2	(breakpoint break-point "break point") with address \$3 near2 compar\$4 with require\$4	US-PGPUB; USPAT	OR	ON	2008/04/15 19:13

S126	0	(breakpoint break-point "break point") with address \$3 near2 compar\$4 with unnecessary	US-PGPUB; USPAT	OR	ON	2008/04/15 19:14
S127	16	manually near2 set\$4 with (breakpoint break-point "break point")	US-PGPUB; USPAT	OR	ON	2008/04/15 19:17
S128	70	(append\$3 attach\$4 add \$3) with (breakpoint break-point "break point") with (bit flag indicat\$3)	US-PGPUB; USPAT	OR	ON	2008/04/16 08:48
S129	20	(append\$3 attach\$4 add \$3) with (breakpoint break-point "break point") with (bit flag indicat\$3)	USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/16 08:48
S130	40	(append\$3 attach\$4 add \$3) with (breakpoint break-point "break point") with (bit flag indicat\$3) and (g06f\$.ipc. debug\$4)	US-PGPUB; USPAT	OR	ON	2008/04/16 08:49
S131	151	(append\$3 attach\$4 add \$3) with break with (bit flag indicat\$3) and (g06f\$.ipc. debug\$4)	US-PGPUB; USPAT	OR	ON	2008/04/16 09:03
S132	20	(append\$3 attach\$4 add \$3) near3 break near3 (bit flag indicat\$3) and (g06f\$.ipc. debug\$4)	US-PGPUB; USPAT	OR	ON	2008/04/16 09:03
S133	25	programmable adj breakpoint	US-PGPUB; USPAT	OR	ON	2008/04/16 10:02
S134	44	"breakpoint bit"	US-PGPUB; USPAT	OR	ON	2008/04/16 10:10
S135	27	"breakpoint bit" with (program\$5 set\$4 clear\$3)	US-PGPUB; USPAT	OR	ON	2008/04/16 10:10
S136	9	"breakpoint bit" with (program\$5 set\$4 clear\$3) with (instruction microinstruction)	US-PGPUB; USPAT	OR	ON	2008/04/16 10:12
S137	22	"breakpoint bit"	USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/16 10:13
S138	994	@pd= "19890927"	EPO	OR	ON	2008/04/16 10:16
S139	4	"software breakpoint"	EPO	OR	ON	2008/04/16 10:26
S140	273	"software breakpoint"	US-PGPUB; USPAT	OR	ON	2008/04/16 10:44

S141	41	"software breakpoint" same (bit flag field) with instruction	US-PGPUB; USPAT	OR	ON	2008/04/16 10:44
S142	26	(halt break breakpoint "break point" break-point) adj (bit flag field) near5 (instruction microinstruction) and debug\$4	US-PGPUB; USPAT	OR	ON	2008/04/16 11:04
S143	3	(halt break breakpoint "break point" break-point) adj (bit flag field) near5 (instruction microinstruction) and debug\$4	USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/16 11:17
S144	0	(halt break breakpoint "break point" break-point) adj (bit flag field) near5 opcode	USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/16 12:44
S145	0	(halt break breakpoint "break point" break-point) with (bit flag field) with opcode	USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/16 12:44
S146	87	(halt break breakpoint "break point" break-point) with (bit flag field) with opcode	US-PGPUB; USPAT	OR	ON	2008/04/16 12:44
S147	25	(halt break breakpoint "break point" break-point) near2 (bit flag field) near5 opcode	US-PGPUB; USPAT	OR	ON	2008/04/16 12:45
S148	2	(halt break breakpoint "break point" break-point) near2 (bit flag field) near5 op-code	US-PGPUB; USPAT	OR	ON	2008/04/16 12:54
S149	1	("breakpoint bit technique")".ti"	IBM_TDB	OR	ON	2008/04/16 13:12
S150	1	"6173386" pn.	US-PGPUB; USPAT	OR	ON	2008/04/16 13:12
S151	1	"6016555" pn.	US-PGPUB; USPAT	OR	ON	2008/04/16 13:13
S152	444	(load\$3 stor\$3) near3 (data content) near3 register with (l-cache "instruction cache" "instruction memory" "instruction RAM")	US-PGPUB; USPAT	OR	ON	2008/04/16 13:23
S153	28	breakpoint with (multiprocessor multi- processor)	US-PGPUB; USPAT	OR	ON	2008/04/16 13:30

S154	53	debug\$4 with stor\$3 with (content data) near2 register with (memory RAM)	US-PGPUB; USPAT	OR	ON	2008/04/16 14:50
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8/ 4/ 08 5:43:37 PM

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